## TRANSMITTAL

## Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Reducing Number of Pins Required to Test Integrated Circuits
Application Number	:
Date :	
First Named Applica	ınt: Mr. Arindam SAHA
Confirmation Number	er:
Attorney Docket Nu	nber: TI-36666

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Submitted By:	Elec. Sign.	Sign. Capacity
Mr. Narendra Reddy Thappeta Registered Number: 41416	Naren Thappeta	Attorney
riegistereu Number. 41416		

Documents being submitted:	Files
us-fee-sheet	txnefiling-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-ids	txnefiling-usidst.xml
	us-ids.dtd
	us-ids.xsl
us-assignment	txnefiling-usassn.xml
as assignment	us-assignment.xsl
	us-assignment.dtd
	assigntxn87BMpg1.tif
	assigntxn87BMpg2.tif
	assigntxn87BMpg3.tif
	assigntxn87HSpg1.tif
	assigntxn87HSpg2.tif
	assigntxn87HSpg3.tif
	assigntxn87pg1.tif
	assigntxn87pg2.tif
	assigntxn87pg3.tif
us-request	txnefiling-usrequ.xml
do request	us-request.dtd
	us-request.xsl
us-declaration	decltxn87HSpg2.tif
us-declaration	decitxn87.tif
application-body	spectxn87-trans.xml
application-body	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent

mmlalias.ent	
soextblx.dtd	
figtxn87_1.tif	
figtxn87_2.tif	
figtxn87_3.tif	

## Comments

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